



TRAN-P012

Patent

2183
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2-6-04
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of

Torvalds et al.

Serial No. 09/417,980

Filing Date: October 13, 1999

For: A METHOD FOR
TRANSLATING
INSTRUCTIONS IN A
SPECULATIVE
MICROPROCESSOR

Examiner: Ellis, Richard L.

Art Unit: 2183

RECEIVED

FEB 03 2004

Technology Center 2100

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

AMENDMENT AND RESPONSE TO OFFICE ACTION

Dear Sir:

In response to the Office Action mailed July 22, 2003, the following amendments and responses to the above captioned patent application are respectfully submitted. Reconsideration of the above captioned patent application is respectfully requested.

01/30/2004 MAHME1 00000072 09417980

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950.00 DP

Serial No. 09/417,980
Examiner: Ellis, Richard L.

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